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Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Geyer, Scott B.
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Title: Methods of Forming Semiconductor Constructions

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References – See Attached Form PTO/SB/08a

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1273 Off. Gaz. Pat. Off. 55, 05 August 2003). No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: April 17, 2006

By: Jennifer J. Taylor
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711